



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Glenn McGall, Lisa Kajisa and Bradley A. Kreger
Application No.: 09/871,610 Group: Not assigned
Filed: June 1, 2001 Examiner: Not assigned
For: New Methods for Array Preparation Using Substrate Rotation

CERTIFICATE OF MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to Assistant Commissioner for Patents, P.O. Box 2327, Arlington, VA 22202	
on <u>3/18/02</u>	<u>Paula Depelteau</u>
Date	Signature
<u>PAULA DEPELTEAU</u>	
Typed or printed name of person signing certificate	

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
P.O. Box 2327
Arlington, VA 22202

Sir:

Please amend the application as follows:

In the Specification

Please replace the paragraph at page 3, line 28 with the following paragraph:

Figure 1 shows the rotation positions for a wafer during array preparation. The position notations are used in the rotation prompts on the computer screen. By using these notations the

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input paddle of the MOS can be labeled A, B, C and D at the sections where the wafer label is supposed to be facing. That way when a certain position is prompted the operator can match the positioning of the wafer label with the letter on the MOS input paddle.

Amendments to the specification are indicated in the attached "Marked Up Version of Amendments" (page i).

REMARKS

The brief description has been amended to include the text of the legend for Figure 1 which previously appeared on the Figure itself. This text has been deleted from the concurrently submitted formal drawing for Figure 1. No new matter has been added.

CONCLUSION

In view of the above amendments and remarks, it is believed that all claims are in condition for allowance, and it is respectfully requested that the application be passed to issue. If the Examiner feels that a telephone conference would expedite prosecution of this case, the Examiner is invited to call the undersigned at (978) 341-0036.

Respectfully submitted,

HAMILTON, BROOK, SMITH & REYNOLDS, P.C.

By Lisa M. Treannie

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Dated: 3/18/02

MARKED UP VERSION OF AMENDMENTSSpecification Amendments Under 37 C.F.R. § 1.121(b)(1)(iii)

Replace the paragraph at page 3, line 28 with the below paragraph marked up by way of bracketing and underlining to show the changes relative to the previous version of the paragraph.

Figure 1 shows the rotation positions for a wafer during array preparation. The position notations are used in the rotation prompts on the computer screen. By using these notations the input paddle of the MOS can be labeled A, B, C and D at the sections where the wafer label is supposed to be facing. That way when a certain position is prompted the operator can match the positioning of the wafer label with the letter on the MOS input paddle.

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